Reusable Formal Verification of DAG-based Consensus Protocols (In TLA+)

Nathalie Bertrand

Rennes University, Inria, CNRS.

Pranav Ghorpade

The University of Sydney.

Sasha Rubin

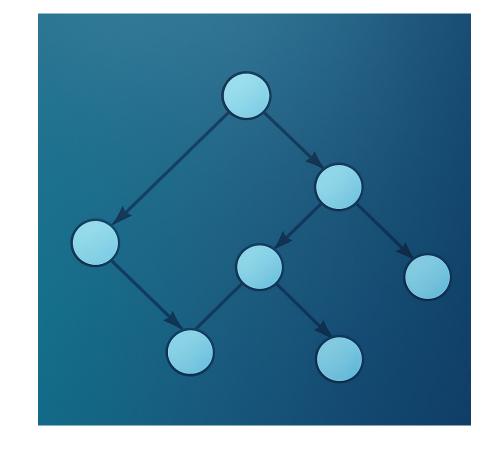
The University of Sydney.

Bernhard Scholz

Sonic Research, The University of Sydney.

Pavle Subotić

Sonic Research.









DAG-based Consensus Protocols

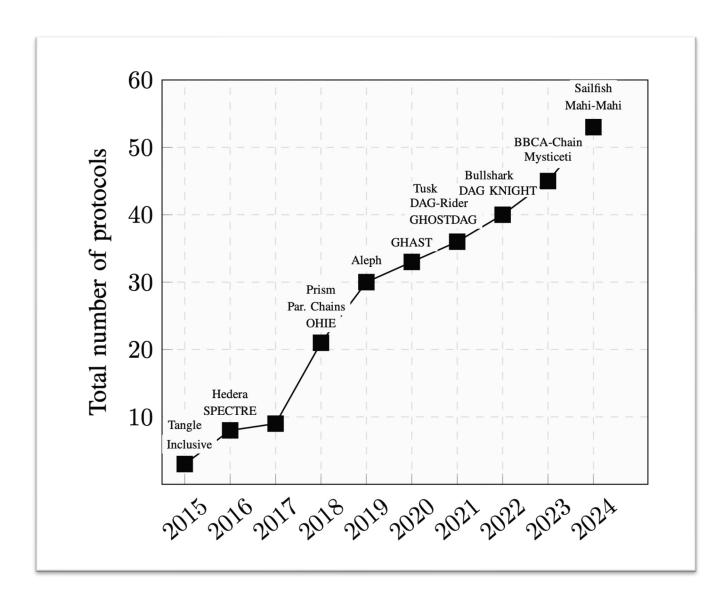


Figure Source:

Raikwar et al., SoK: DAG-based Consensus Protocols, IEEE ICBC 2024.

DAG-based Consensus Protocols

Improvements over Classical Blockchains:

- High performance
- ☐ Low communication complexity
- Byzantine fault tolerance

Used in Many Modern Blockchains:







- Testing: Often misses corner-case interleavings.
- Model Checking: Does not scale to real-world instances.
- Parameterized Model Checking: Not yet expressive enough.

Theorem Proving - the only viable option for rigorous verification!

☐ Proving correctness of each DAG-based consensus protocol from scratch is infeasible.

☐ Proving correctness of each DAG-based consensus protocol **from scratch is infeasible**.

☐ We demonstrate that DAG-based consensus protocols are amenable to **practical**, **reusable**, and **compositional** formal methods.

- **☑** Safety Verified Specifications in TLA+:
 - DAG-Rider
 - Cordial Miners
 - Hashgraph
 - Eventually Synchronous BullShark
 - A variant of Aleph

With proof effort reduced by almost 50%

DAG-based Consensus Protocols Solve the Byzantine Atomic Broadcast Problem

- ☐ N processes; some may be Byzantine-faulty.
- Each process can propose and output blocks.
- All correct processes eventually output same set of blocks and in the same order (**Agreement, Total order**).
- ☐ No correct process outputs same block more than once (Integrity).
- ☐ A block proposed by a correct process is eventually output by all correct processes (**Validity**).

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DAG-based Consensus Protocols Solve the Byzantine Atomic Broadcast Problem in Two Phases

DAG Construction Phase

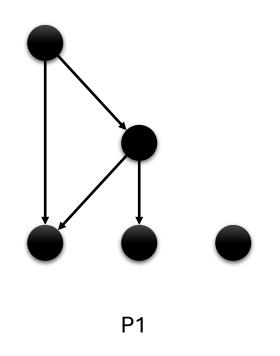
Processes **communicate** their blocks and **build a DAG** of exchanged blocks.

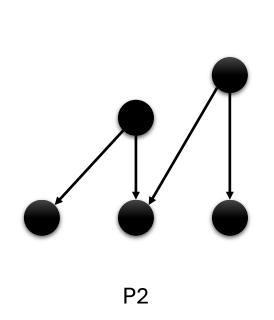


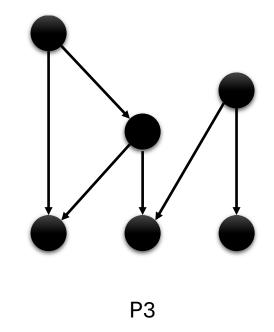
Ordering Phase

Processes use their locally constructed DAGs to determine the **total order** of the blocks.

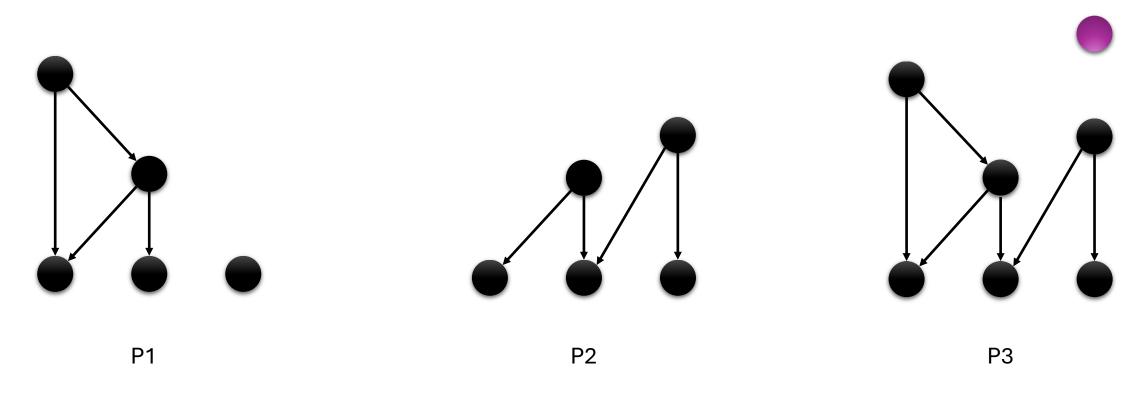
Processes Communicate Their Blocks and Build a DAG of Exchanged Blocks





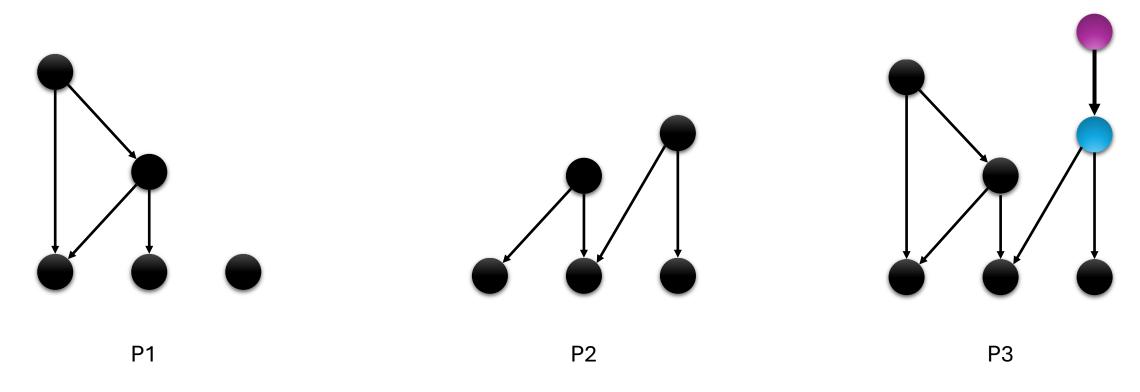


Processes Communicate Their Blocks and Build a DAG of Exchanged Blocks



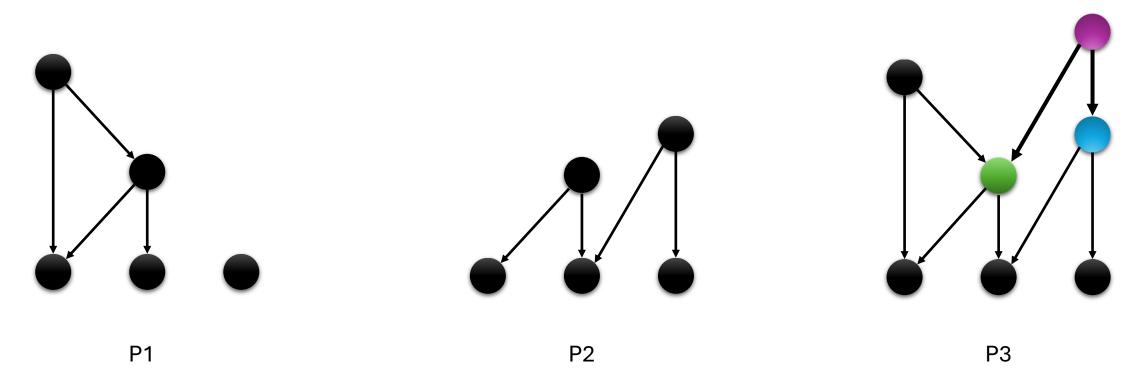
A process creates new blocks in the form of vertices.

Processes Communicate Their Blocks and Build a DAG of Exchanged Blocks



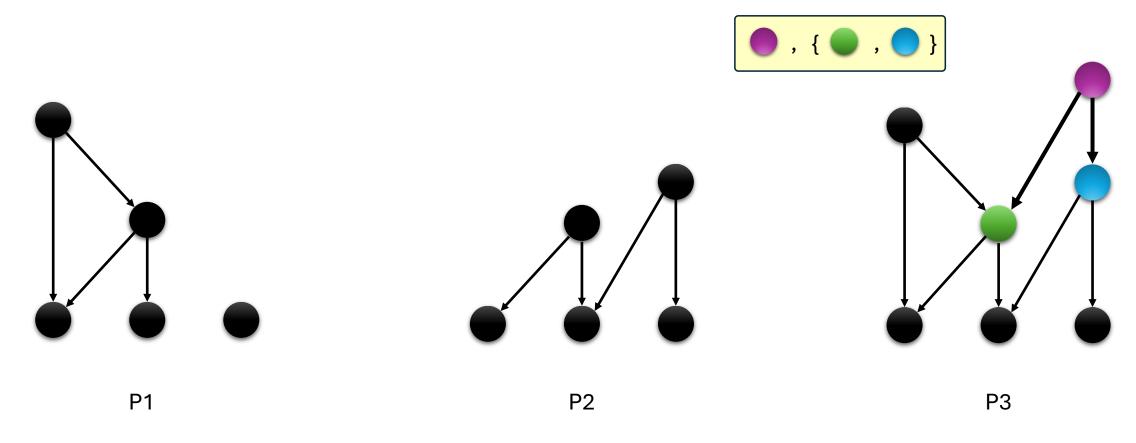
A process creates a new vertex by referencing it to its last vertex and other vertices in its local DAG.

Processes Communicate Their Blocks and Build a DAG of Exchanged Blocks



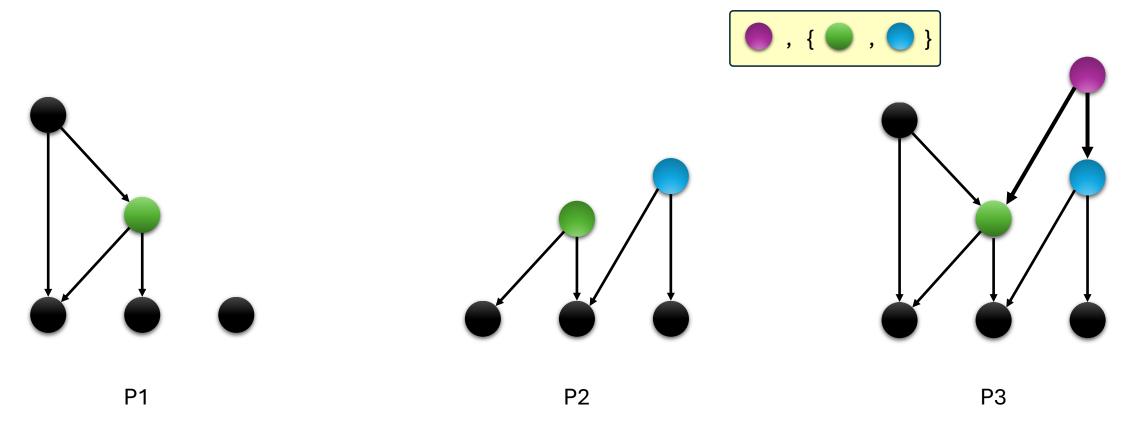
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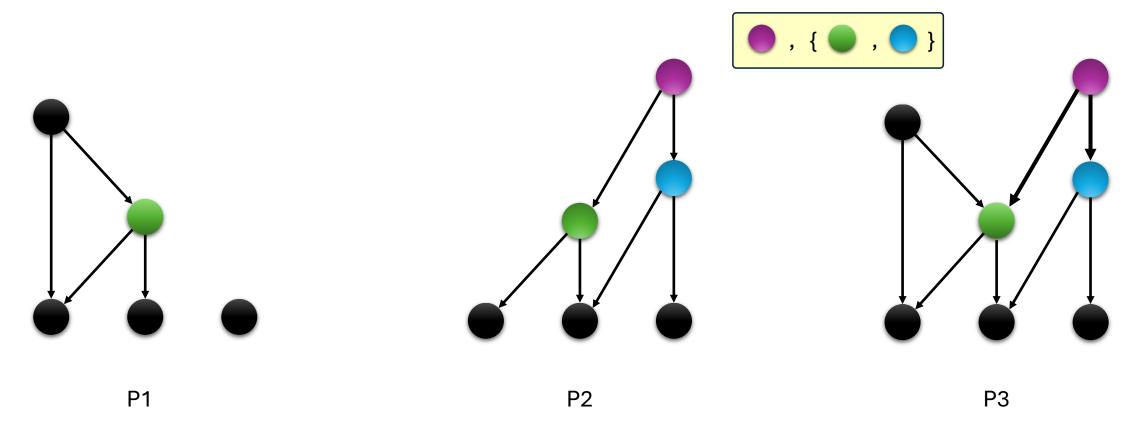
A process communicates newly created vertices along with their references to other processes.

Processes Communicate Their Blocks and Build a DAG of Exchanged Blocks



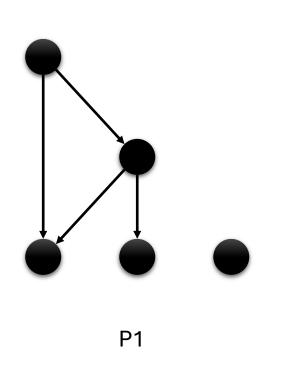
When a process receives a vertex from another processes, it checks whether it has all its references in its local DAG.

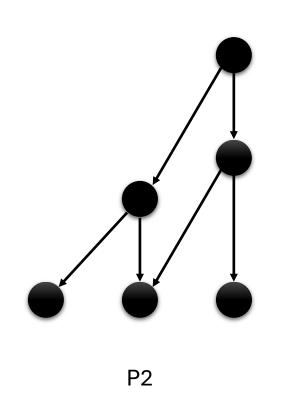
Processes Communicate Their Blocks and Build a DAG of Exchanged Blocks

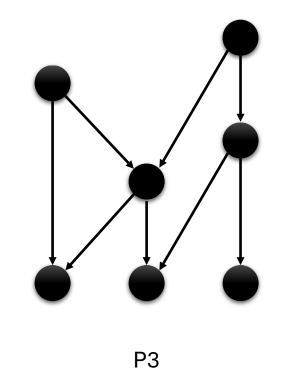


If the references exist in the process's local DAG, it adds the vertex; otherwise, it stores it in a buffer.

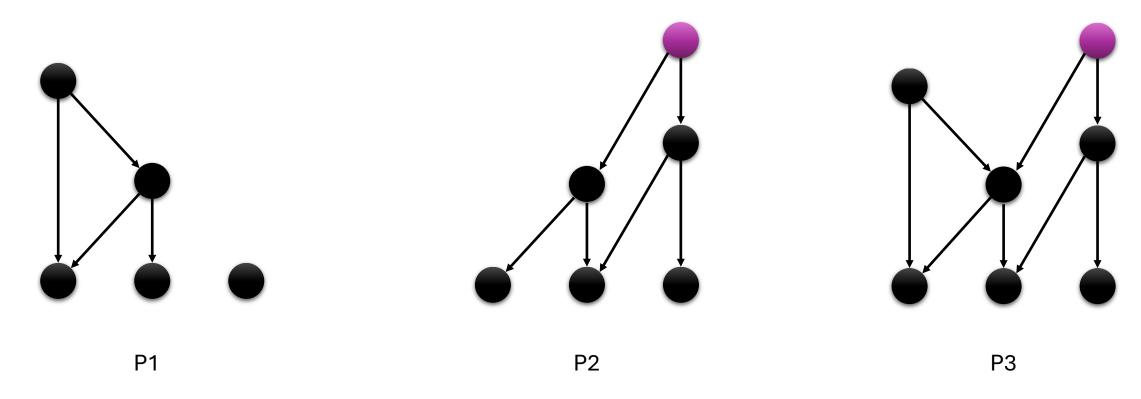
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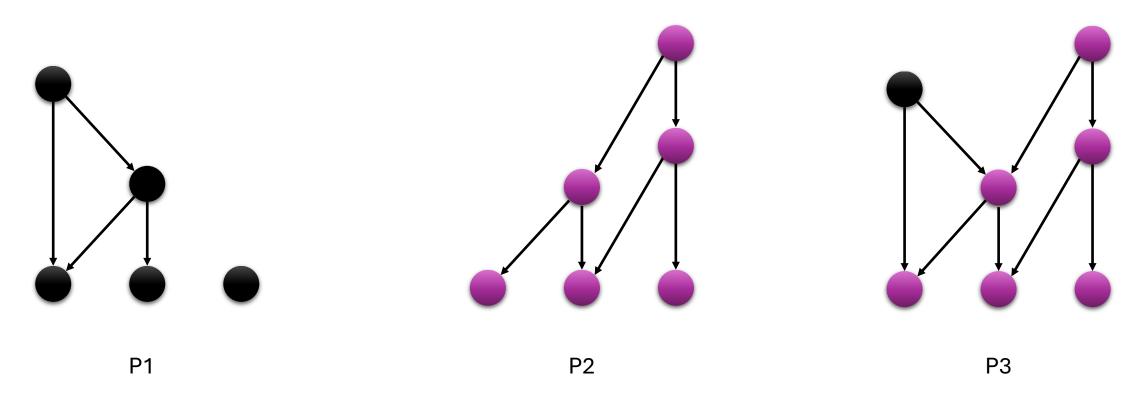


DAG Construction Ensures the Consistent Causal History Property



If a vertex is present in the local DAG of two correct processes, then their causal histories are the same.

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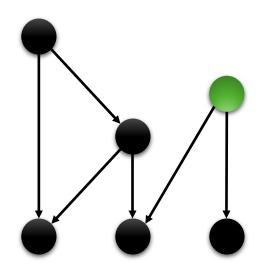
	DAG Construction Phase	
Protocol	Comm- unication	DAG-Type
DAG-Rider		
Cordial Miners		
ES BullShark		
Aleph		
Hashgraph		

	DAG Construction Phase	
Protocol	Comm- unication	DAG-Type
DAG-Rider	Reliable (RB)	
Cordial Miners	Unreliable	
ES BullShark	Reliable (RB)	
Aleph	Reliable (RB)	
Hashgraph	Unreliable	

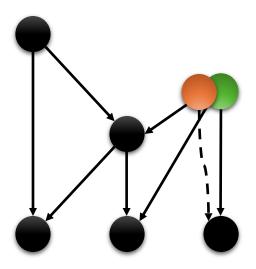
RB: Reliable Broadcast

Reliable vs. Unreliable Communication

Reliable Communication (reliable broadcast)



Unreliable Communication (plain broadcast/ gossip)



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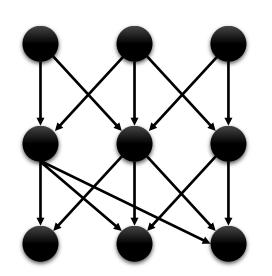
RB: Reliable Broadcast

	DAG Construction Phase		
Protocol	Comm- unication	DAG-Type	
DAG-Rider	Reliable (RB)	Structured	
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Aleph	Reliable (RB)	Structured	
Hashgraph	Unreliable	Unstructured	

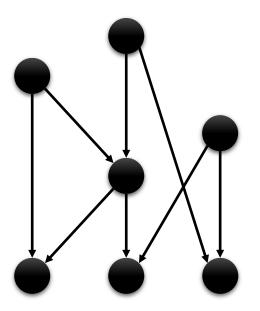
RB: Reliable Broadcast

Structured vs. Unstructured DAG Construction

Structured DAG Construction (round-driven)



Unstructured DAG Construction (event-driven)



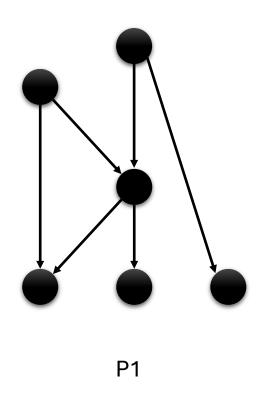
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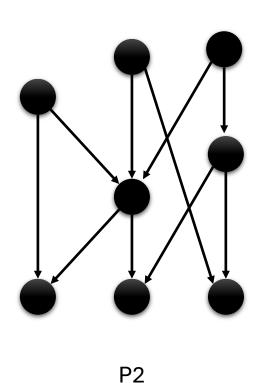
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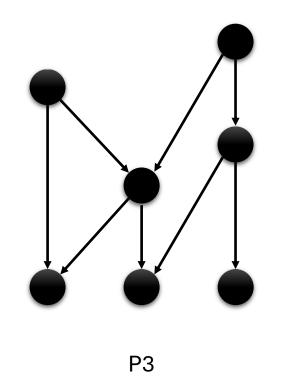
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RB: Reliable Broadcast

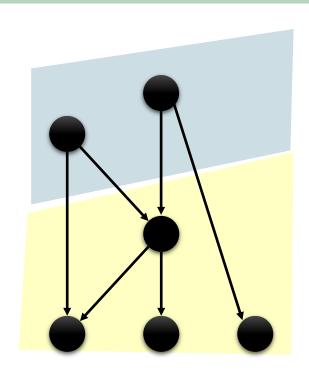
Processes Use Their Local DAGs to Determine the Total Order of the Vertices

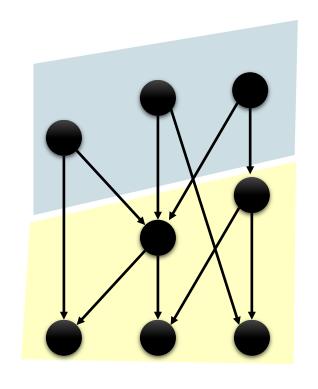


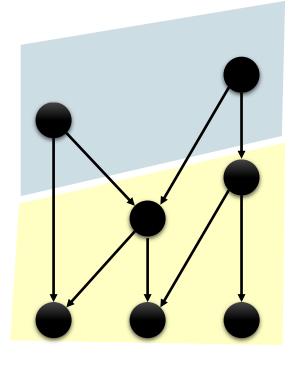




Frame Construction: Partitioning DAGs into Sequential Frames



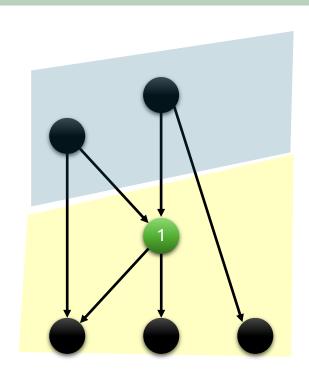


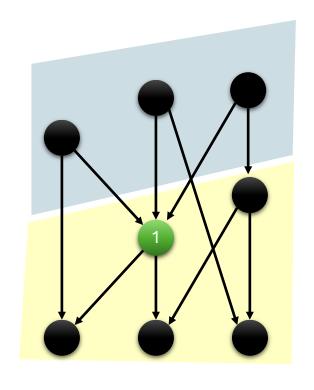


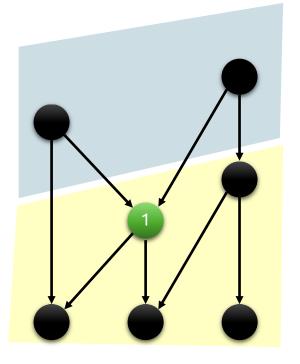
P1

P2

Anchor Selection: Agree on a Vertex per Frame





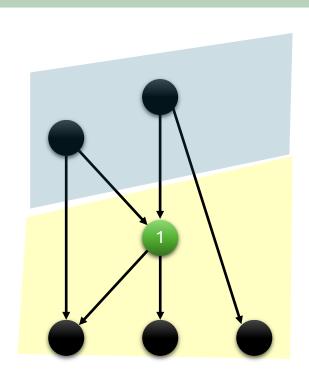


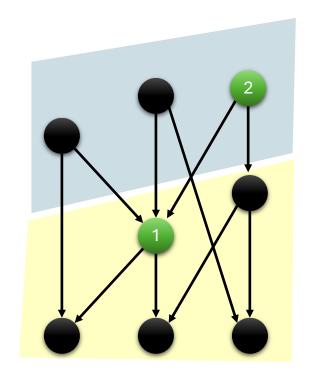
P1

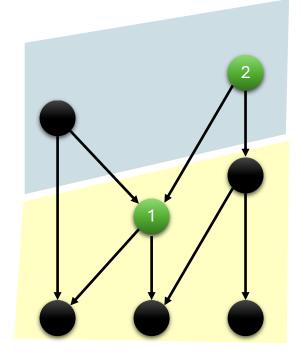
P2

Р3

Anchor Selection: Agree on a Vertex per Frame



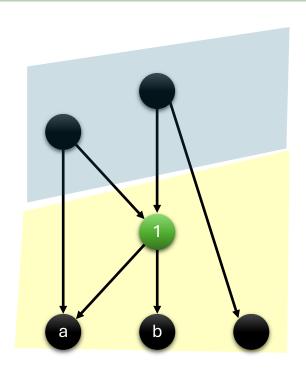


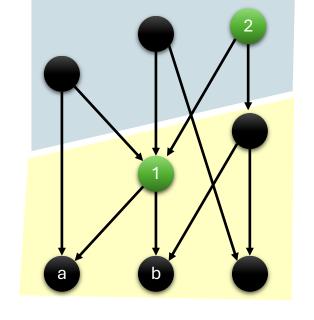


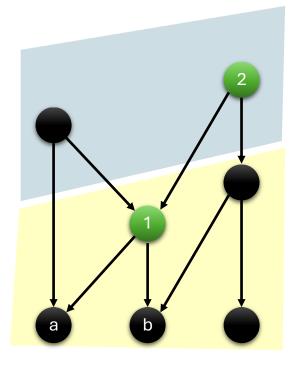
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P1

Total Ordering: Using the Causal Histories of Anchor Vertices







P1













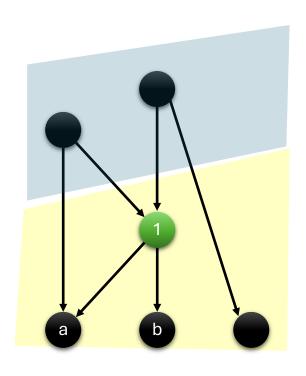
P2

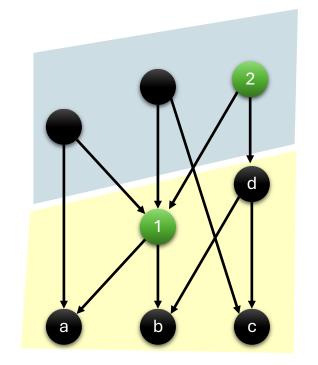




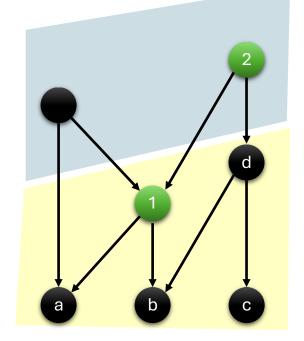


Total Ordering: Using the Causal Histories of Anchor Vertices





P2



P3



































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RB: Reliable Broadcast

	DAG Const	ruction Phase		Ordering Phase
Protocol	Comm- unication	DAG-Type	Anchor Agreement	
DAG-Rider	Reliable (RB)	Structured	GPC-based	
Cordial Miners	Unreliable	Structured	GPC-based	
ES BullShark	Reliable (RB)	Structured	GPC-style (deterministic)
Aleph	Reliable (RB)	Structured	Virtual Voting based	
Hashgraph	Unreliable	Unstructured	Virtual Voting based	

RB: Reliable Broadcast, GPC: Global Perfect Coin

	DAG Constr	DAG Construction Phase			
Protocol	Comm- unication	DAG-Type	Anchor Agreement	Fork Handling	
DAG-Rider	Reliable (RB)	Structured	GPC-based	-	
Cordial Miners	Unreliable	Structured	GPC-based	Required	
ES BullShark	Reliable (RB)	Structured	GPC-style (deterministic)	-	
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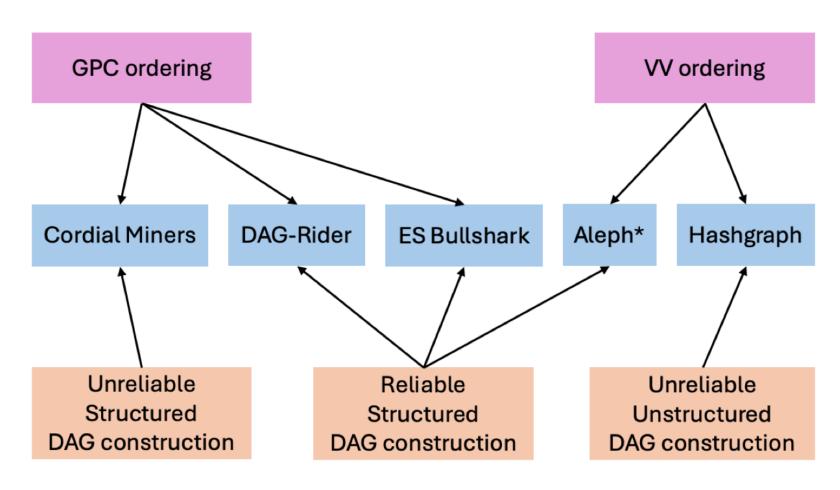
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Building Blocks and Their Use in Verifying Five DAG-based Consensus Protocols



GPC: Global Perfect Coin, VV: Virtual Voting

Specifications and Proofs in TLA+ (Checked with TLAPS)

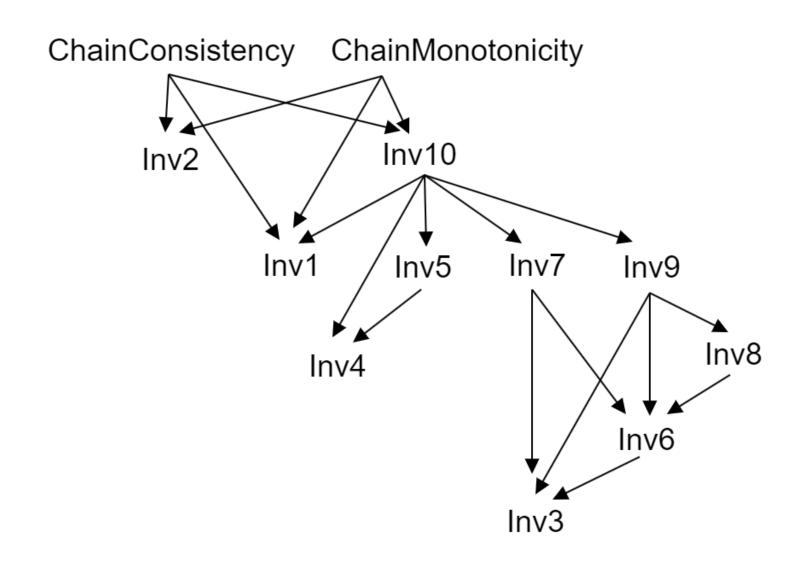
Why TLA+?

- ☐ Produces a specification closer to the
 - implementation.
- Produces execution traces.
- ☐ Supports interface refinement.

Specifications and Proofs in TLA+ (Checked with TLAPS)

- ☐ Specify safety properties as safety invariants.
- ☐ In TLA+ Invariants are proved by induction.
- ☐ The hardest challenge: identify relevant inductive invariants that imply the safety invariants.

Verifying Safety Invariants of GPC Ordering



Proof of an Invariant in TLA+

```
329 LEMMA IndInv5Lem == Spec => []IndInv5
330 <1>1 Init => IndInv5
           BY DEF Init, InitBlocksToPropose, InitBroadcastNetwork, InitBroadcastRecord, InitBuffer, InitDag, InitRound, IndInv5
332 <1>2 ASSUME [Next] vars, StateType, StateType', IndInv5, IndInv2
333
           PROVE IndInv5
334
           <2>1 ASSUME NEW p \in ProcessorSet, NEW b \in BlockSet, ProposeTn(p, b)
335
                PROVE IndInv5'
336
                BY VertexSetDefPlt, <2>1, <1>2 DEF IndInv5, ProposeTn
337
           <2>2 ASSUME NEW p \in ProcessorSet, NextRoundTn(p)
338
                PROVE IndInv5'
                <3>1 ASSUME NEW r \in RoundSet, NEW v \in VertexSet, Broadcast(p, r, v)
339
340
                     PROVE IndInv5'
341
                     <4>1 CASE broadcastRecord[p][r] = FALSE
342
                          <5>1 broadcastNetwork'["History"] = broadcastNetwork["History"] \cup {[sender |-> p, inRound |-> r, vertex |-> v]}
343
                               BY <3>1, <2>2, <1>2, <4>1 DEF StateType, BlocksToProposeType, BroadcastNetworkType, BroadcastRecordType, BufferType, DagType, RoundType, Broadcast
344
                          <5>2 broadcastRecord' = [broadcastRecord EXCEPT ![p][r] = TRUE]
345
                               BY <3>1, <2>2, <1>2, <4>1 DEF StateType, BlocksToProposeType, BroadcastNetworkType, BroadcastRecordType, BufferType, DagType, RoundType, Broadcast
346
                          <5>3 ASSUME NEW m \in broadcastNetwork'["History"], NEW o \in broadcastNetwork'["History"], m.sender = o.sender, m.inRound = o.inRound
347
348
                               <6>1 CASE m \in broadcastNetwork["History"] /\ o = [sender |-> p, inRound |-> r, vertex |-> v]
349
                                    <7>1 broadcastRecord[m.sender][m.inRound] = TRUE
350
                                         BY <6>1, <1>2 DEF IndInv2
351
                                    <7> QED BY <4>1, <7>1, <6>1, <5>3
352
                               <6>2 CASE o \in broadcastNetwork["History"] /\ m = [sender |-> p, inRound |-> r, vertex |-> v]
353
                                    <7>1 broadcastRecord[o.sender][o.inRound] = TRUE
354
                                         BY <6>2, <1>2 DEF IndInv2
355
                                    <7> QED BY <4>1, <7>1, <6>2, <5>3
356
                               <6>3 CASE m \in broadcastNetwork["History"] /\ o \in broadcastNetwork["History"]
357
                                    BY <6>3, <5>3, <1>2 DEF IndInv5
358
                               <6> QED BY <6>1, <6>2, <6>3, <5>3, <5>1
359
                          <5> QED BY <1>2, <5>3 DEF IndInv5
360
                     <4>2 CASE broadcastRecord[p][r] = TRUE
361
                          <5>1 UNCHANGED <<br/>broadcastNetwork, broadcastRecord>>
362
                               BY <4>2, <2>2, <3>1 DEF Broadcast
363
                          <5> QED BY <5>1, <1>2 DEF IndInv5
364
                     <4> QED BY <4>1, <4>2, <3>1, <2>2, <1>2 DEF StateType, BlocksToProposeType, BroadcastNetworkType, BroadcastRecordType, BufferType, DagType, RoundType
365
                <3> QED BY VertexSetDefPlt, <2>2, <1>2, CreateVertexTypePlt, <3>1 DEF IndInv5, NextRoundTn, Broadcast
           <2>3 ASSUME NEW p \in ProcessorSet, NEW r \in RoundSet, NEW q \in ProcessorSet, NEW v \in VertexSet, p# q, ReceiveVertexTn(p, q, r, v)
366
367
                PROVE IndInv5'
368
                <3>1 broadcastNetwork'["History"] = broadcastNetwork["History"]
369
                     <4>1 p # "History"
370
                          BY <2>3, ProcSetAsm DEF ProcessorSet
371
                     <4> QED BY <4>1, <2>3, <1>2 DEF StateType, BlocksToProposeType, BroadcastNetworkType, BroadcastRecordType, BufferType, DagType, RoundType, ReceiveVertexTn
372
                <3> QED BY <3>1, VertexSetDefPlt, <2>3, <1>2 DEF IndInv5, ReceiveVertexTn
373
           <2>4 ASSUME NEW p \in ProcessorSet, NEW v \in VertexSet, AddVertexTn(p, v)
374
                PROVE IndInv5'
375
                BY VertexSetDefPlt, <2>4, <1>2 DEF IndInv5, AddVertexTn
376
           <2>5 CASE UNCHANGED vars
377
                BY VertexSetDefPlt, <2>5, <1>2 DEF IndInv5, vars
           <2> QED BY <1>2, <2>1, <2>2, <2>3, <2>4, <2>5 DEF Next
379
      <1> QED BY <1>1, <1>2, TypeLem, IndInv2Lem, PTL DEF Spec
380
```

Evaluation

Motrie \ Phage	Reliable	Unreliable	Unreliable	GPC	VV
Metric \ Phase	structured	structured	unstructured	Ordering	Ordering
Size of spec. (# loc)	403	160	230	272	136
Number of invariants	6	6	7	10	18
Size of proof ($\#$ loc)	460	594	554	822	2120
Max level of proof tree nodes	10	9	8	9	13
Max degree of proof tree nodes	7	8	7	7	11
# obligations in TLAPS	633	895	665	1302	3316
Time to check by TLAPS (s)	49	68	74	125	651

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Approach and Insight:

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Potential Impact on Verifying Other DAG-based Protocols:

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